

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Jack A. Mandelman, et al.

Examiner: Unassigned

Serial No: Unassigned

Art Unit: Unassigned

Filed: Herewith

Docket: FIS920000224US2(13814A)

For: METHOD FOR FORMING DUAL  
WORKFUNCTION HIGH-PERFORMANCE  
SUPPORT MOSFETs IN EDRAM ARRAYS

Dated: May 22, 2001

Assistant Commissioner for Patents  
United States Patent and Trademark Office  
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

In connection with filing the above-identified application under 37 C.F.R. §1.56(b), applicants submit the following amendments and remarks for consideration by the Examiner and entry of record in the above-identified patent application.

IN THE SPECIFICATION:

Page 1, after the heading ‘DESCRIPTION’ please insert the following:

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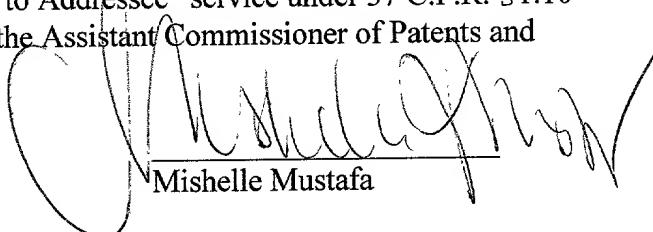
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Dated: May 22, 2001



Mishelle Mustafa

--This application is a division of U.S. Application Serial No. 09/706,492 filed on November 3, 2000.--

**IN THE CLAIMS:**

Please cancel Claims 1-52, without prejudice or disclaimer.

**REMARKS**

Favorable consideration and allowance of the claims of the present application, as presently filed, are respectfully requested.

In this preliminary amendment, applicants have cancelled original Claims 1-52 herein since those claims are the subject matter of parent application U.S. Serial No. 09/706,492 filed November 3, 2000 and have amended the specification to include information regarding the parent application.

Wherefore, consideration and allowance of the claims of the present application are respectfully requested.

Respectfully submitted,



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LSS:gc